

**Amendments to the Claims:**

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. - 11. (Cancelled)
12. (Previously Presented) An organic EL device, comprising:
  - a first and second set of electrodes;
  - an organic EL layer formed above each of the first and second set of electrodes;
  - an effectively optical area in which the first set of electrodes is formed;
  - a dummy area disposed around the effectively optical area, and not over the first set of electrodes in which the second set of electrodes is formed; and
  - the second set of electrodes not being connected to any driving circuit elements.
13. (Previously Presented) An organic EL device, comprising:
  - a set of electrodes;
  - an effectively optical area in which the electrodes are formed;
  - a dummy area disposed around the effectively optical area;
  - an organic EL layer formed above each of the electrodes and above the dummy area; and
  - a bank layer disposed between each electrode of the set of electrodes, the organic EL layer in the dummy area being disposed on a layer made of a same material as the bank layer.
14. (Previously Presented) The organic EL device according to claim 13, the bank layer including an organic bank layer and an inorganic bank layer, and the organic EL layer in

the dummy area being disposed on a layer made of a same material as the inorganic bank layer.

15. (Previously Presented) The organic EL device according to claim 14, the bank layer being disposed laterally between portions of the organic EL layer in the dummy area.

16. (Previously Presented) The organic EL device according to claim 13, the bank layer including an organic bank layer, the organic EL layer in the dummy area being disposed on a layer made of a same material as the organic bank layer.

17. (Previously Presented) An organic EL device, comprising:  
a first and second set of electrodes, the first set of electrodes being connected to at least one thin film transistor, while the second set of electrodes is not connected to at least one thin film transistor;  
an organic EL layer formed above each of the first and second set of electrodes;  
an effectively optical area in which the first set of electrodes is formed; and  
a dummy area disposed around the effectively optical area, in which the second set of electrodes is formed, the organic EL layer in the dummy area being disposed on the second set of electrodes.

18. (Previously Presented) The organic EL device according to claim 17, further including a bank layer being formed laterally between portions of the organic EL layer in the dummy area.

19. (Currently Amended) An organic EL device, comprising:  
a first and second set of electrodes;  
an organic EL layer formed above each of the first and second set of electrodes;  
an effectively optical area in which the first set of electrodes is formed; and

a dummy area disposed around the effectively optical area, in which the second set of electrodes is formed, adjacent ~~portions~~portions of the organic EL layer being disposed at a constant pitch in both the effectively optical area and the dummy area.

20. (Previously Presented) The organic EL device according to claim 12, both the effectively optical area and the dummy area being provided on a substrate, and portions in the effectively optical area on the substrate having a substantially same cross-sectional structure as portions in the dummy area on the substrate.

21. (Previously Presented) An organic EL device, comprising:  
an effectively optical area having a first set of electrodes;  
a second set of electrodes formed outside the effectively optical area;  
the second set of electrodes not being connected to any driving circuit elements; and  
an organic EL layer formed on each of the first and second set of electrodes.

22. (Cancelled).

23. (Previously Presented) An electronic device, comprising:  
the organic EL device according to claim 12.

24. (Previously Presented) An EL device comprising:  
an optically active area including a plurality of pixels,  
a first EL layer disposed in the optically active area;  
a dummy area including a dummy pixel, being adjacent to the optically active area, the dummy pixel not being electrically connected to the plurality of pixels; and  
a second EL layer disposed in the dummy area.

25. (Previously Presented) An EL device according to claim 24, further comprising a bank that divides the optically active area into the plurality of pixels.

26. (Previously Presented) An EL device according to claim 25, wherein the bank is formed in the dummy area,

wherein the first EL layer is provided in a region partitioned by the bank, and

wherein the second EL layer is provided on the bank in the dummy area.

27. (Previously Presented) An EL device according to claim 25, wherein the bank includes a plurality of layers.

28. (Previously Presented) An EL device according to claim 25, further comprising a electrode in an area partitioned by the bank, the first EL layer being provided on the electrode.

29. (Previously Presented) An EL device according to claim 27, wherein the bank includes at least a SiO<sub>2</sub> layer and a polyimide layer.

30. (Previously Presented) An EL device according to claim 26, wherein the bank is formed in the dummy area, and the bank divides the dummy area into a plurality of dummy pixels.

31. (Previously Presented) An EL device according to claim 30, the second EL layer is provided in the area partitioned by the bank.

32. (Previously Presented) An EL device according to claim 31, wherein the bank includes at least a SiO<sub>2</sub> layer and a polyimide layer,

wherein the dummy pixels are divided by the polyimide layer, and

wherein the second EL layer is disposed on the SiO<sub>2</sub> layer.

33. (Previously Presented) An EL device according to claim 24, further comprising:

a substrate disposed under the plurality of pixels and dummy pixel;

a circuit element portion including a TFT, and provided between the first EL layer and the substrate; and

a bank that partitions the plurality of pixels, wherein the TFT overlaps with the bank.

34. (Previously Presented) An EL device according to claim 24, further comprising a plurality of dummy pixels, wherein the dummy pixels and the pixels are substantially equal in pitch.

35. (Previously Presented) An EL device according to claim 24, further comprising a plurality of dummy pixels, wherein the dummy pixels are arranged in lines.

36. (Previously Presented) An EL device according to claim 24, wherein a plurality of optically active areas are provided, each of which are surrounded by dummy areas.

37. (Previously Presented) An EL device according to claim 24, wherein the first EL layer includes at least hole injection material and a luminous material.

38. (Previously Presented) An EL device according to claim 24, wherein the second EL layer includes at least hole injection material and a luminous material.